File   View   Edit   Tools   Window   Help

Drafts
Pending
Active
    L1: (0) mram same (ram adj buffer) same reduc
    L2: (0) (mram same (ram adj buffer)) and redu
    L3: (0) (mram same (ram adj buffer)) and redu
    L4: (0) mram and (ram adj buffer) and reduce a
    L5: (141) mram and ram and buffer and reduce
    L6: (35) mram and ram and buffer and (reduce

DBs   USPAT, US-PGPUB, EPO, JPO   Plurals
Default operator   OR   Highlight all hit terms initially

BRS terms   IS&R term   Image   Text   HTML

	Type	L #	Hits	Search Text	DBs	
1	BRS	L1	0	mram same (ram adj buffer) same reduce same power same flip-flop	USPAT; US-PGPUB; EPO; JPO	20
2	BRS	L2	0	(mram same (ram adj buffer)) and reduce and power and flip-flop	USPAT; US-PGPUB; EPO; JPO	20
3	BRS	L3	0	(mram same (ram adj buffer)) and reduce and power	USPAT; US-PGPUB; EPO; JPO	20
4	BRS	L4	0	mram and (ram adj buffer) and reduce and power	USPAT; US-PGPUB; EPO; JPO	20
5	BRS	L5	141	mram and ram and buffer and reduce and power	USPAT; US-PGPUB; EPO; JPO	20
6	BRS	L6	35	mram and ram and buffer and (reduce near4 power)	USPAT; US-PGPUB; EPO; JPO	20

Hits   Details   HTML

Ready   NUM